

METHOD OF FABRICATING AN INTEGRATED CIRCUIT THROUGH UTILIZING METAL LAYERS TO PROGRAM RANDOMLY POSITIONED BASIC UNITS

Abstract

A method of fabricating an integrated circuit. The integrated circuit has a semiconductor body. The method includes forming a plurality of basic units with the same component characteristic on the semiconductor body, and forming at least a layout layer to program the basic units for building a clocked logic circuit and a non-clocked logic circuit without placing restrictions on positions of the clocked logic circuit and the non-clocked logic circuit on the semiconductor body.